

Arun Mehta

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Santa Clara, CA

Experience

Weiser Security, Northern CA
September 2025 - Current
Security Officer

Working at Snap Inc as a Flex Lobby Ambassador at their Palo Alto and San Francisco locations. Responsible for access control, issuing temporary and visitor badges, office interior patrol, communicating with GSOC for permanent access control authorizations and responding to incidents and emergencies.

Live Nation, Mountain View, CA
August 2025 - October 2025
Security Officer

Worked the 2025 season of events at Shoreline Amphitheatre. Responsible for bag checks, secondary metal detection, guarding the venue gates and emergency exits, patrolling the interior lawn area and the venue parking lot for artists.

Allied Universal, Burlingame, CA
October 2024 - July 2025
Security Officer

Worked as a security officer at the Rakuten campus in San Mateo. Responsible for greeting employees and visitors, enforcing access control, conducting regular patrols, issuing citations for parking violations and providing assistance in case of emergency.

Securitas, San Jose, CA
February 2024 - October 2024
Security Officer

Worked as a screening officer in the data center. Responsible for following SOP(Standard Operating Procedure) to protect the data. Document the activities and the incidents in the shift report in accordance with the SOP.

Persenc Software, Santa Clara, CA
February 2005 - Current
Owner

Data encryption using Python, Go, Java.
Encrypted email providing end-to-end encryption based on industry standards, OpenPGP, RSA and AES. Implementation using Go programming language with web user interface. Previous versions were implemented using Java with Swing graphical user interface.

Neso Technology, Santa Clara, CA

January 1998 - October 2001

Owner

Website development using HTML, CSS and Javascript. Dynamic web application development using MySQL and PostgreSQL databases.

MotelValues Online, Santa Clara, CA

November 1995 - December 1997

Owner

Web advertising service focusing on the motel market segment.

Quickturn Design Systems, Inc. Mountain View, CA

October 1995 - November 1995

Product Marketing Engineer

Product management for Logic Analyzer emulator and interface products. Beta testing for HDL-ICE product.

Silicon Graphics Inc. / MIPS Technologies Inc. Mountain View, CA

June 1991 - September 1995

MTS, Technical Marketing

Product specialist for R4400/R4000 microprocessors. Responsible for managing strategic alliances with semiconductor and system partners. Responsible for communicating technical and business information to global partners. Participated in trade shows.

Micro Computing Solutions, Santa Clara, CA

October 1990 - June 1991

Owner

PC hardware and software reseller for both Windows/DOS and UNIX market.

MIPS Computer Systems, Sunnyvale, CA

June 1989 - September 1990

Member of Technical Staff

Fault simulation for the R3010 floating-point processor. Evaluated initial fault coverage and implemented additional tests for improving the coverage.

Intel Corporation, Santa Clara, CA

May 1984 - May 1989

Senior Engineer

Participated in Random Instruction Testing for the i860 microprocessor. Involved in customer interface, design methodology, logic simulation, place and route and interface with manufacturing for ASIC gate array business. Developed and used UNIX based automation tools for i386 production test development. Participated in debugging of production test program and developed diagnostic programs in x86 assembly language for functional verification of i386.

Martek Inc. Westmont, IL

January 1984 - April 1984

Software Engineer

Worked on a PC-based integrated package for small to medium size PCB manufacturing operation.

Education

Executive MBA, University of San Francisco, San Francisco, CA

August 1993 - June 1995

A team oriented approach to management education for experienced professionals.

University of Texas at Austin, Austin, TX

September 1980 - December 1983

Graduate study in Electrical Engineering with focus on robotics.

MSEE, Illinois Institute of Technology, Chicago, IL

June 1979 - August 1980

Master's degree in Electrical Engineering with emphasis on digital systems design.

BE(E), Maharaja Sayajirao University, Vadodara, India

June 1973 - June 1978

Bachelor's degree in Engineering with broad Electrical Engineering curriculum.

Skills

Python for AI

Go, Java, C

HTML, CSS, JavaScript

PostgreSQL Database

Linux, Bash scripting

Security

California Guard Card